

FIG. 1
PRIOR ART

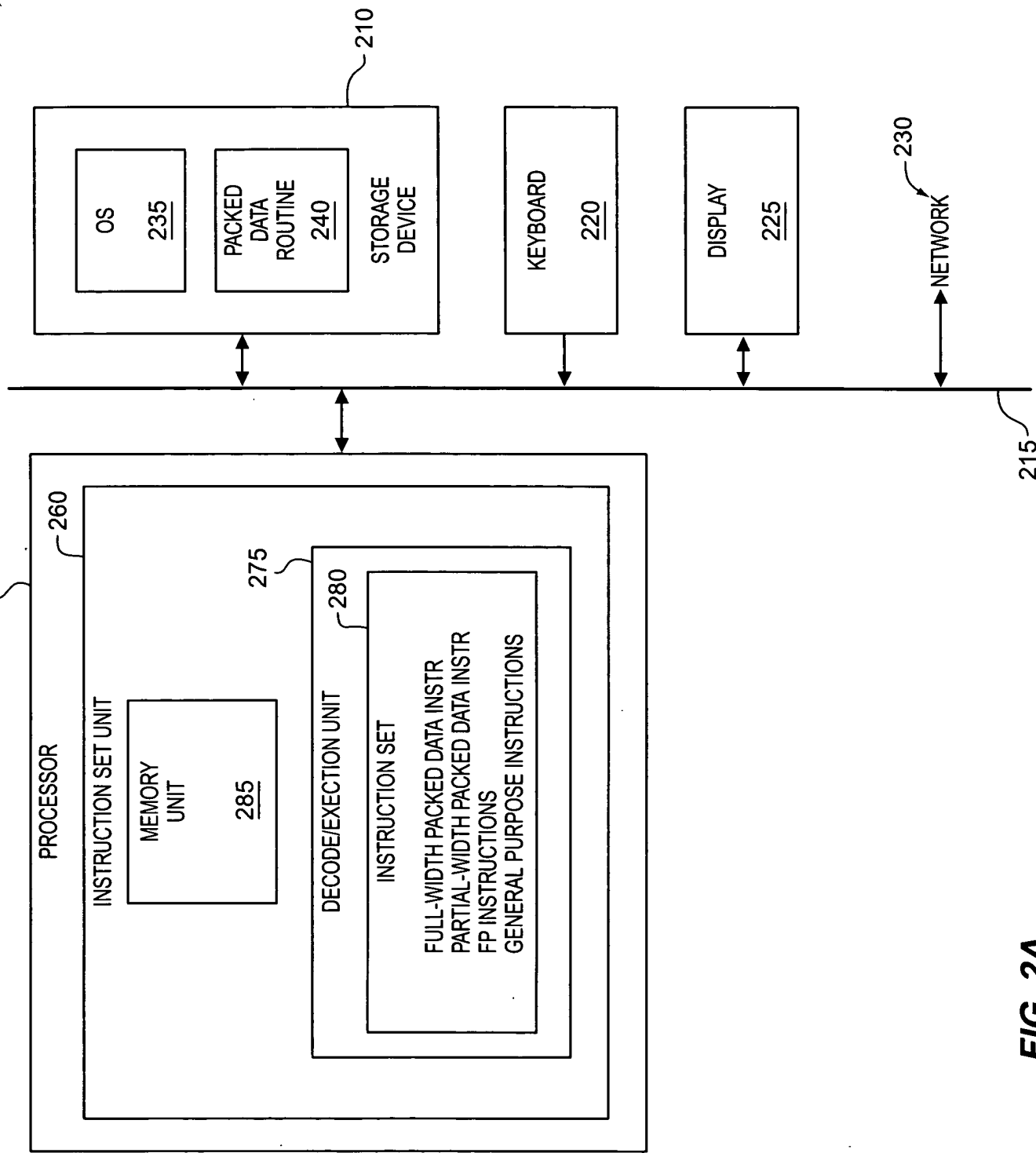


FIG. 2A

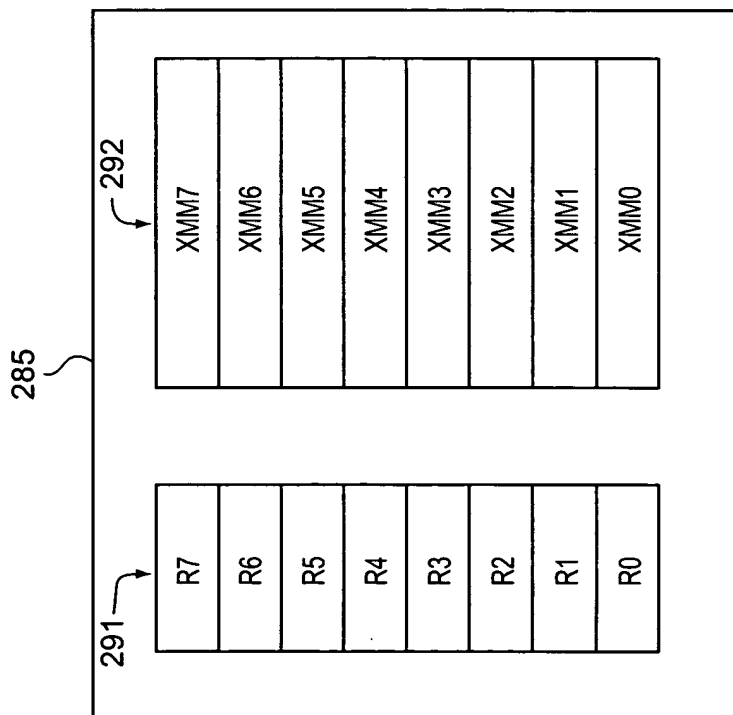


FIG. 2B

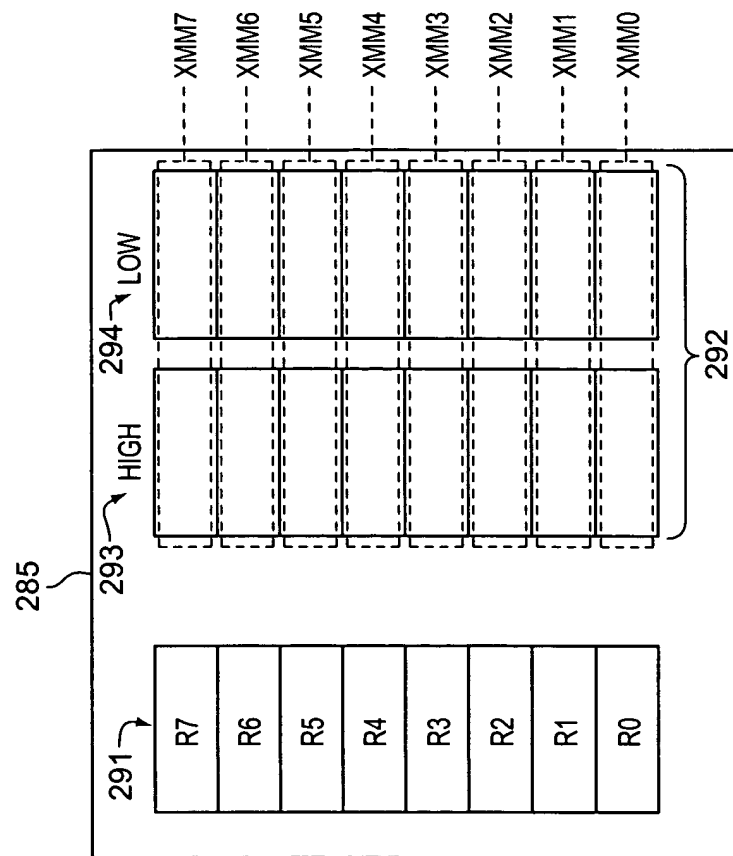


FIG. 2C

098521.050801
TOP SECRET

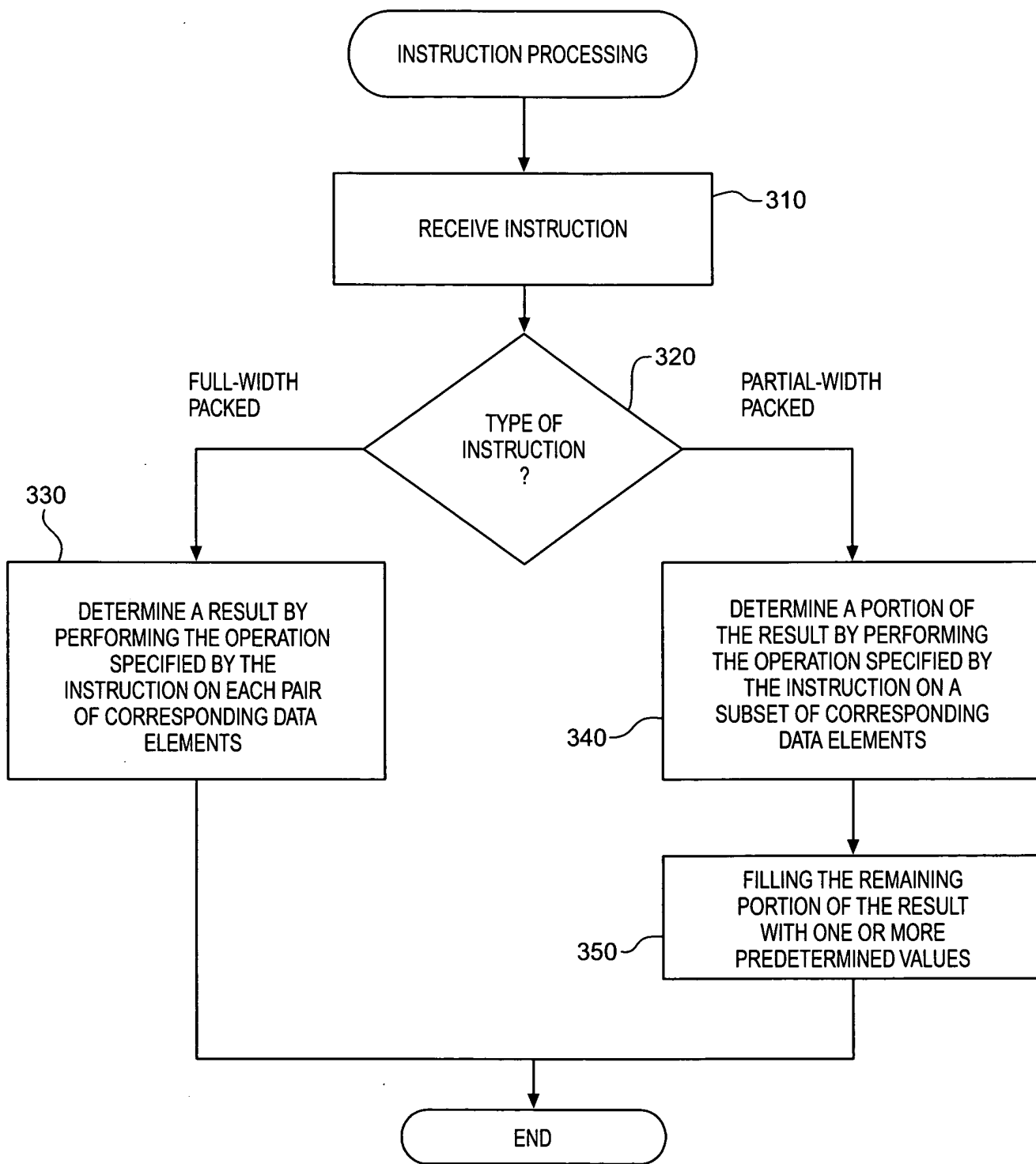


FIG. 3

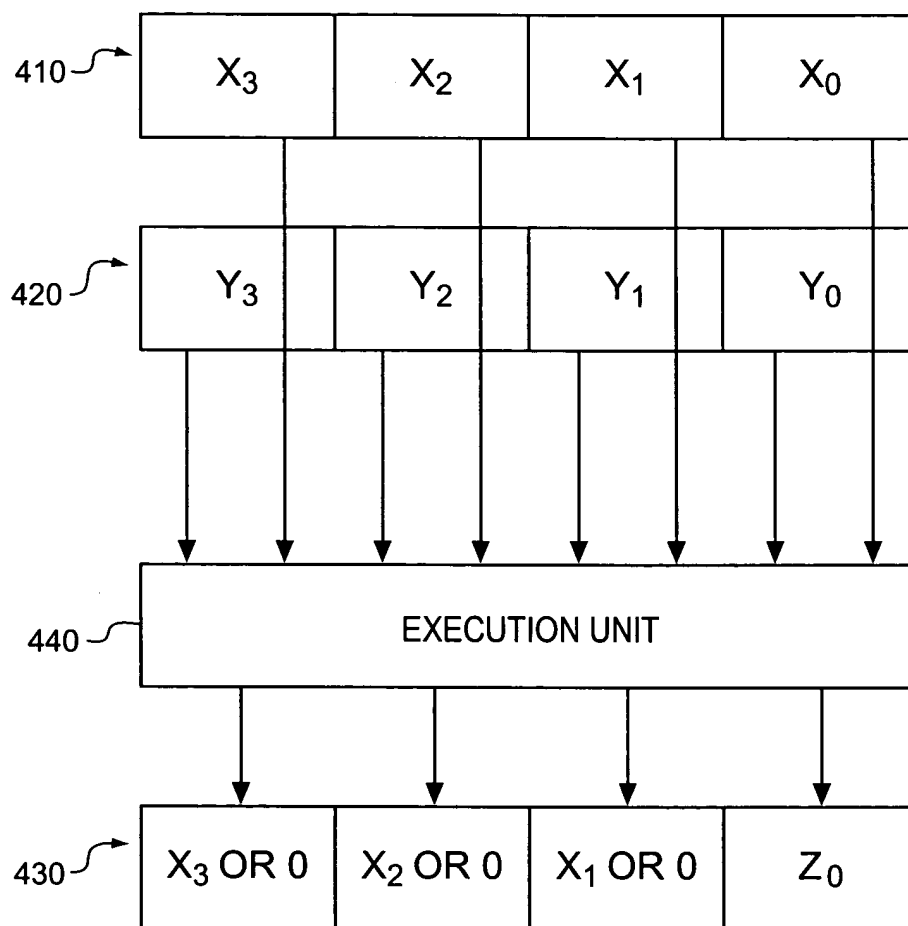


FIG. 4

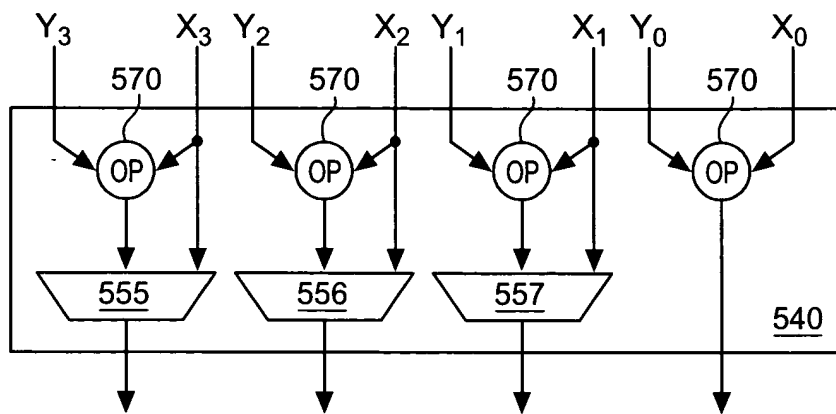


FIG. 5A

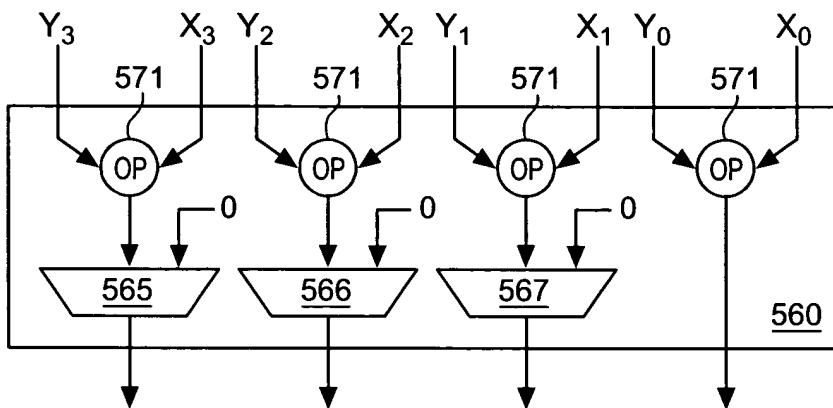


FIG. 5B

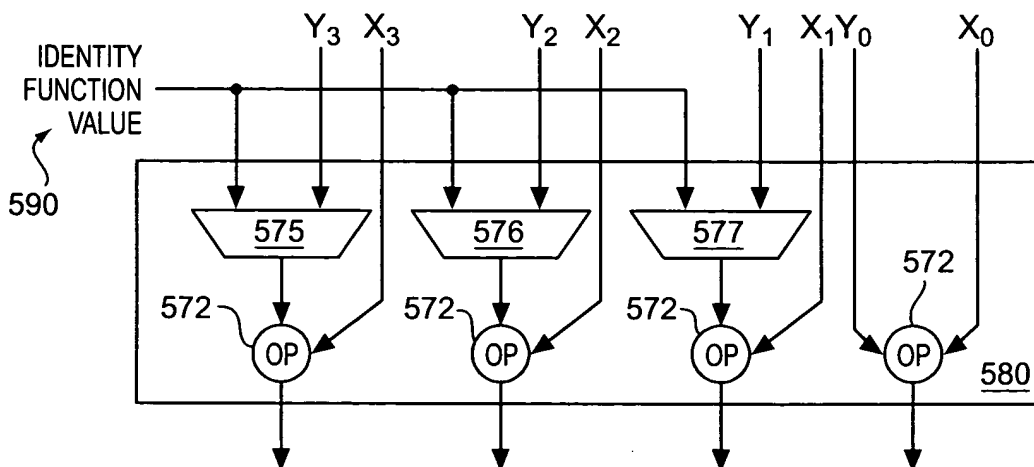


FIG. 5C

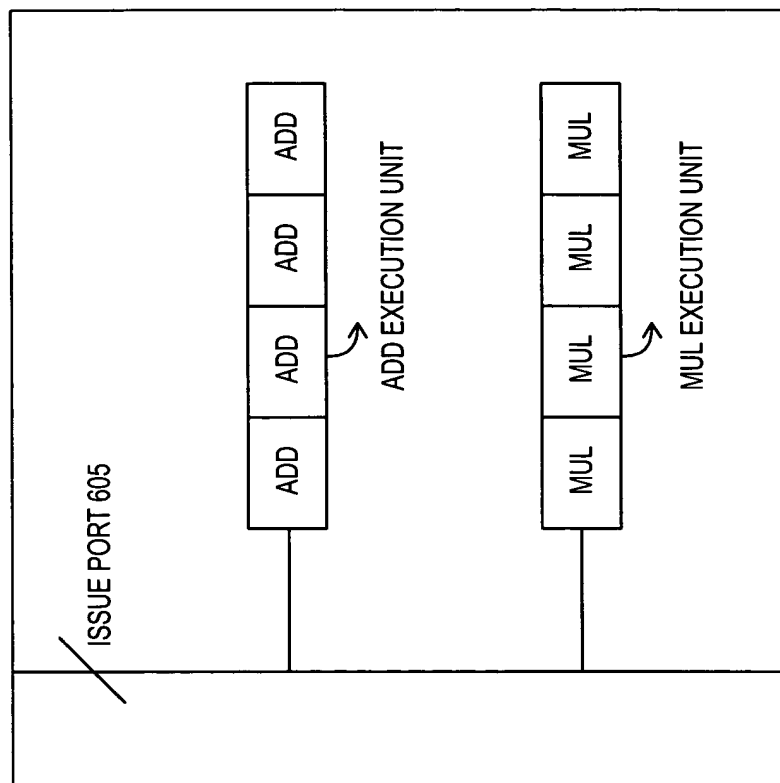
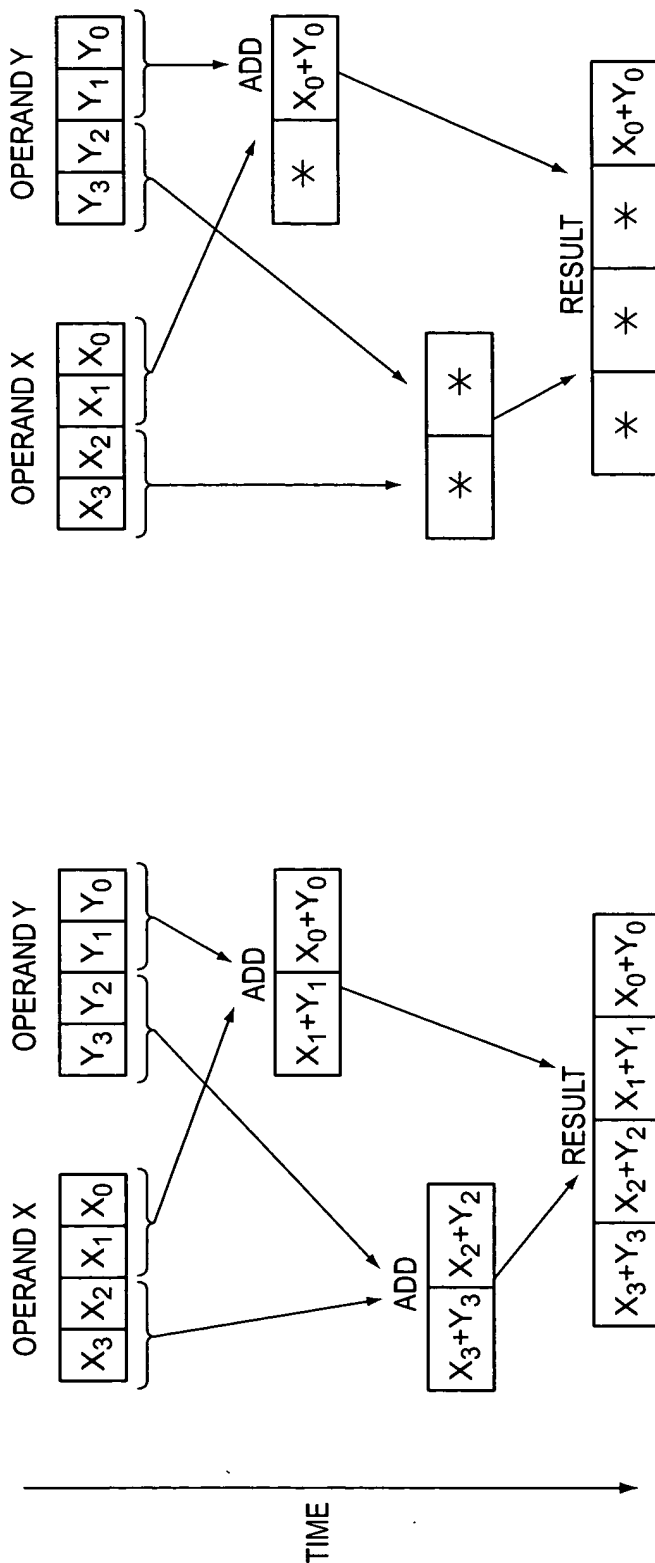
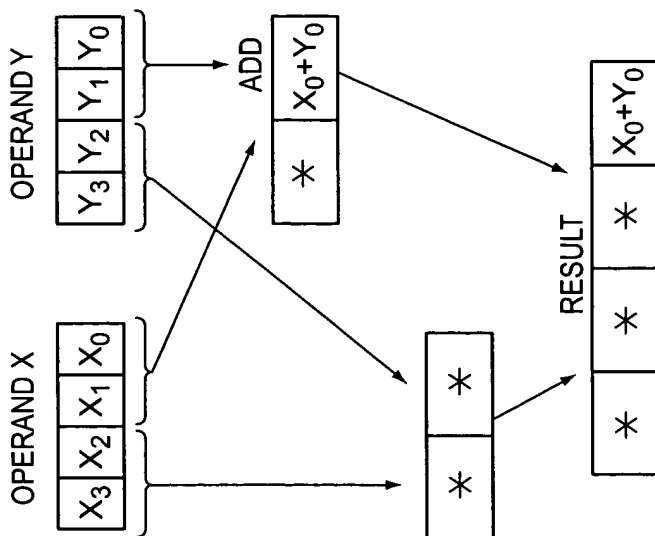


FIG. 6



* = CORRESPONDING DATA ELEMENT
IN X OR Y, NaN, 0, OR OTHER
PREDETERMINED VALUE

FIG. 7B



TOP SECRET

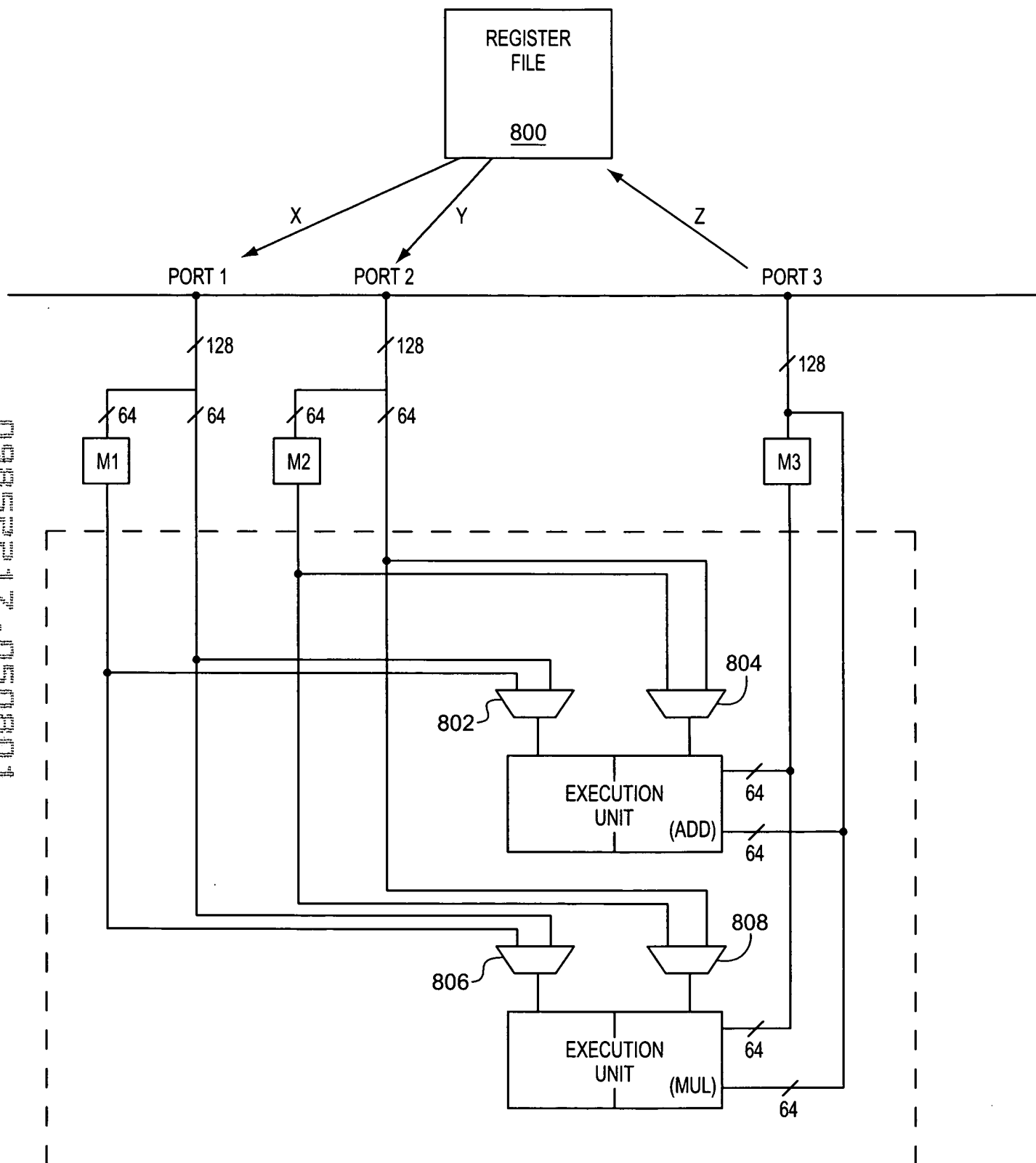


FIG. 8A

TIME	128-BIT INSTRUCTION	PERFORMED ON 64-BIT DATA
T	ADD X, Y	ADD X ₀ Y ₀ ADD X ₁ Y ₁
T+1		ADD X ₂ Y ₂ ADD X ₃ Y ₃
T+1	MUL X, Y	MUL X ₀ Y ₀ MUL X ₁ Y ₁
T+2		MUL X ₂ Y ₂ MUL X ₃ Y ₃

FIG. 8B

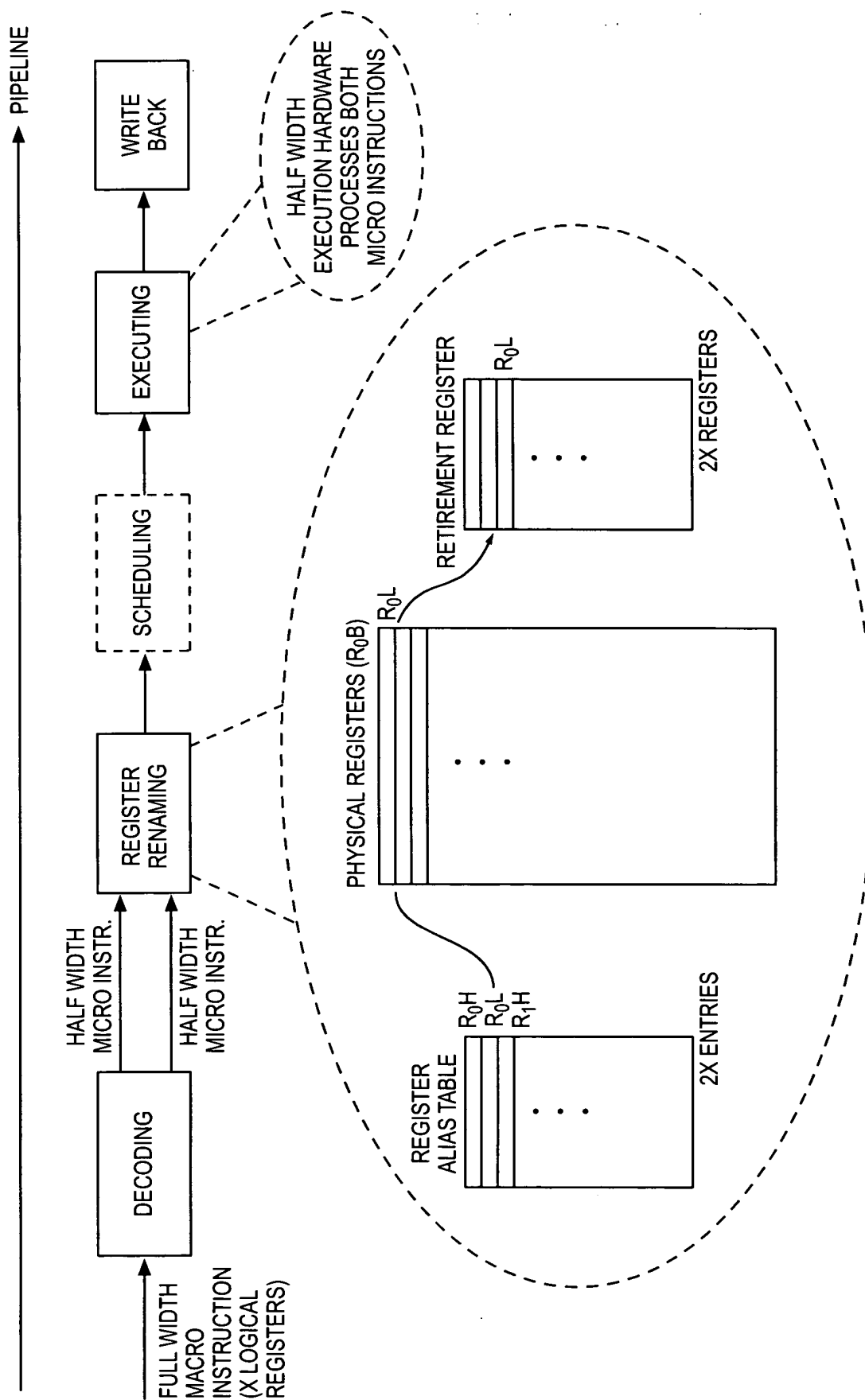


FIG. 9

TIME	128-BIT INSTRUCTION	64-BIT INSTRUCTION
T	ADD X,Y	ADD X _L ,Y _L
T+N		ADD X _H ,Y _H

FIG. 10

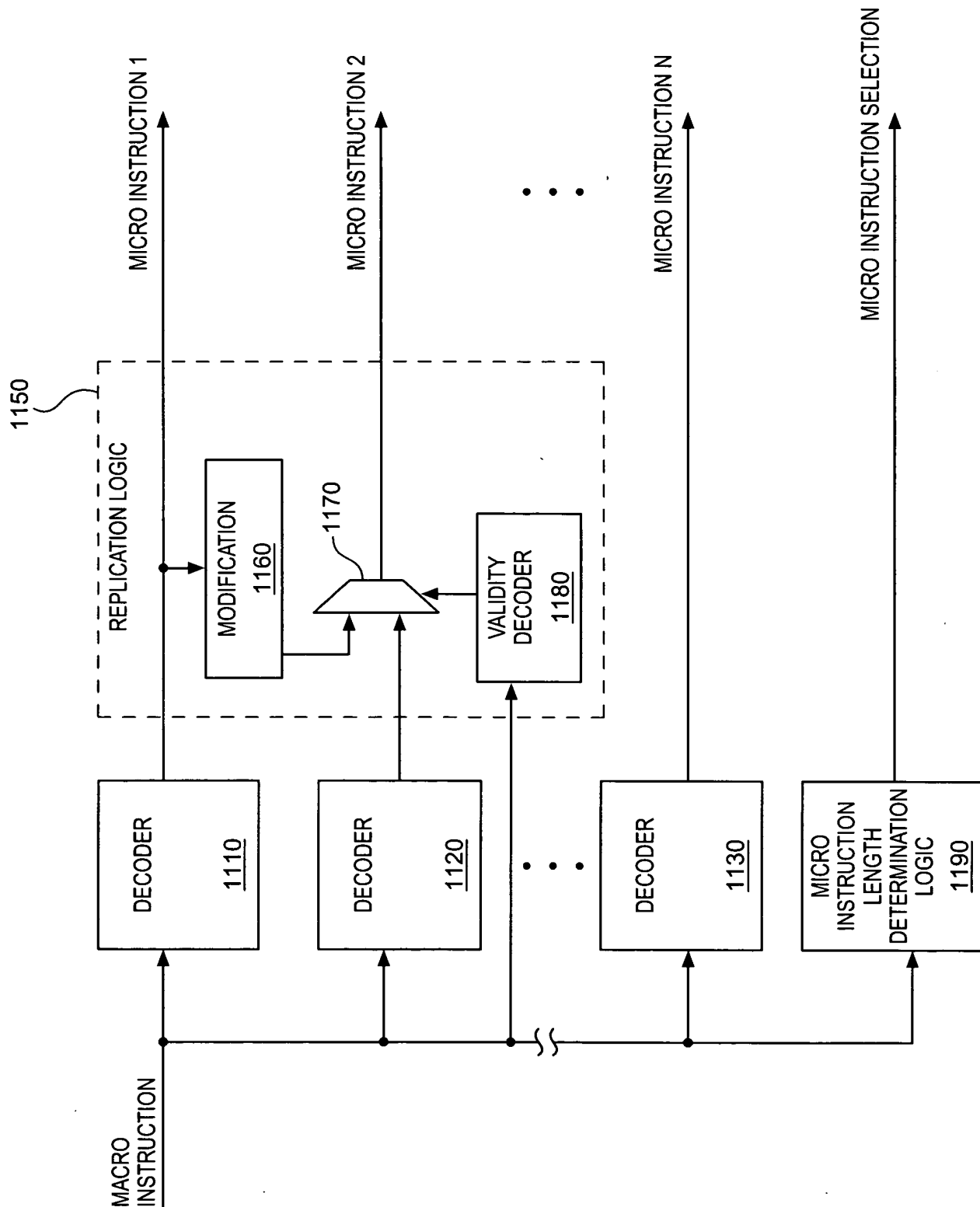


FIG. 11